Notice of Allowability	Application No.	Applicant(s)	
	10/643,996	GOTO, AKIO	
	Examiner	Art Unit	
	Jerry T Rahll	2874	
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communicatio GHTS. This application is subject	oplication. If not included in will be mailed in due co	urse. THIS
1. This communication is responsive to application received 2	20 August 2003.		
2. The allowed claim(s) is/are <u>1-15</u> .			
3. The drawings filed on 20 August 2003 are accepted by the	Examiner.		
4. Acknowledgment is made of a claim for foreign priority una All b) Some* C None None Some* Some* Some* Some* None None None Some* Some	been received. been received in Application No cuments have been received in this of this communication to file a reply ENT of this application. itted. Note the attached EXAMINER as reason(s) why the oath or declar t be submitted. on's Patent Drawing Review (PTO as Amendment / Comment or in the element of the draw the header according to 37 CFR 1.121 sit of BIOLOGICAL MATERIAL	r complying with the requirement of the requirement	TICE OF
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/03 Paper No./Mail Date 8/20/03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☐ Examiner's Amend	ate	ŕ

Examiner's Statement of Reasons for Allowance

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 20 August 2003 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Drawings

3. The drawings submitted on 20 August 2003 have been reviewed and determined to facilitate understanding of the invention. The drawings are accepted as submitted.

Allowable Subject Matter

- 4. The following is an examiner's statement of reasons for allowance. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 5. Claims 1-15 are allowed. Claims 1-15 describe a card type optical transceiver module including an optical receptacle having a photoelectric component for converting electrical signals into optical and optical signals into electrical signals, an electrical connector electrically connected to an electronic device, a motherboard and a childboard which are separately formed and electrically connect the optical receptacle and the electrical connector, a first cutaway portion provided in the motherboard, and a group of pins electrically connect the boards. The

Application/Control Number: 10/643,996 Page 3

Art Unit: 2874

optical receptacle is supported in a housing forming an outer contour of the module. The first cutaway portion accepts the optical receptacle inside to avoid interference between the optical receptacle and the signals and the motherboard. The childboard is supported by the housing, and the motherboard is supported by the group of pins and incorporated in the housing, and the electrical connector is provided on an edge surface of the housing.

- 6. U.S. Patent No. 6,402,393 to Grimes et al. describes a card type optical transceiver module including an optical receptacle having a photoelectric component for converting electrical signals into optical and optical signals into electrical signals, an electrical connector electrically connected to an electronic device, a motherboard and a childboard which are separately formed and electrically connect the optical receptacle and the electrical connector and a group of pins electrically connect the boards. The electrical connector is provided on an edge surface of the housing. Grimes et al. does not describe or reasonably suggest a first cutaway portion provided in the motherboard to accept the optical receptacle so as to avoid interference between the optical receptacle and the motherboard.
- 7. U.S. Patent No. 6,402,393 to Grimes et al. remains the closest prior art of record in this application. For the reasons stated above, however, Claims 1-15 herein are deemed to patentably distinguish over Grimes et al. and all other prior art of record.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents Nos. 5,493,437 and 6,213,651 describe card type optical transceivers.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerry T Rahll whose telephone number is (571) 272-2356. The examiner can normally be reached on M-F (8:00-5:30), with alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rodney Bovernick can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jerry T Rahll